

INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Not for submission under 37 CFR 1.99)</i>	Application Number		09989777
	Filing Date		2001-11-19
	First Named Inventor		Craig Nemecek
	Art Unit		2123
	Examiner Name		Mary C. Jacob
	Attorney Docket Number		CD01208M

U.S.PATENTS						
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	1	5633766	B1	1997-05-27	Hase et al.	Entire Document
	2	5399922	B1	1995-03-21	Kiani et al.	Entire Document
	3	5680070	B1	1997-10-21	Anderson et al.	Entire Document
	4	4138671	B1	1997-02-06	Comer et al.	Entire Document
	5	6157270	B1	2000-12-05	Tso	Entire Document
	6	6166367	B1	2000-12-26	Cho	Entire Document
	7	5600262	B1	1997-02-04	Kolze	Entire Document
	8	5414308	B1	1995-05-09	Lee et al.	Entire Document

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9	5258760	B1	1993-11-02	Moody et al.	Entire Document
10	5563526	B1	1996-10-08	Hastings et al.	Entire Document
11	6225866	B1	2001-05-01	Kubota et al.	Entire Document
12	5880598	B1	1999-03-09	Duong	Entire Document
13	6304101	B1	2001-10-16	Nishihara	Entire Document
14	5426378	B1	1995-06-20	Ong	Entire Document
15	5828693	B1	1998-10-27	Mays et al.	Entire Document
16	5703871	B1	1997-12-30	Pope et al.	Entire Document
17	6018559	B1	2000-01-25	Azegami et al.	Entire Document
18	6191660	B1	2001-02-20	Mar et al.	Entire Document
19	5939949	B1	1999-08-17	Olgaard et al.	Entire Document

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	2	0450863A2	EP	A1	1991-10-09	Austin	Entire Document	<input type="checkbox"/>
	3	0499383A2	EP	A1	1992-08-19	El Ayat; Khaled, A.	Entire Document	<input type="checkbox"/>
	4	0308583A2	EP	A1	1989-03-29	Bartlett	Entire Document	<input type="checkbox"/>
	5	0639816A2	EP	A1	1995-02-22	McCollum	Entire Document	<input type="checkbox"/>
	6	1170671A1	EP	A1	2002-01-08	Arena et al.	Entire Document	<input type="checkbox"/>

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	7	1205848	EP	A1	2002-05-15	Ericsson	Entire Document	<input type="checkbox"/>
	8	1191423A2	EP	A1	2003-02-01	Garnell I Andreu	Entire Document	<input type="checkbox"/>
	9	404083405	JP	A1	1992-03-17	Yasue	Entire Document	<input type="checkbox"/>
	10	404095408	JP	A1	2002-03-27	Nishimaki	Entire Document	<input type="checkbox"/>
	11	405055842	JP	A1	1993-03-26	Kondo	Entire Document	<input type="checkbox"/>
	12	368398	EP	A1	1990-05-01	Thommen	Entire Document	<input type="checkbox"/>
	13	06021732	JP	A1	1994-01-28	Kadowaki, Tadao	Entire Document	<input type="checkbox"/>

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	1	From Application Number 10/033,027 (CD00232): Goodenough, F. "Analog Counterparts of FPGAS Ease System Design" Electronic Design, Penton Publishing, Cleveland, OH, US vol. 42, no. 21, 10/14/94; 10 pages	<input type="checkbox"/>
	2	From Application Number 10/033,027 (CD00232): Harbaum, T. et al. "Design of a Flexible Coprocessor Unit" Proceedings of the Euromicro Conference, XX XX, Sept. 1999; 10 pages	<input type="checkbox"/>

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3	From Application Number 10/033,027 (CD00169); "Programmable Microcontroller (PSoC) Architecture (Mixed Analog/Digital)"; 08/07/01; 09/924,734 Snyder et al.; 28 pages	<input type="checkbox"/>
4	From Application Number 10/033,027; CD00170: "Digital Configurable Macro Architecture"; 07/18/01; 09/909,045; Snyder; 37 pages	<input type="checkbox"/>
5	From Application Number 10/033,027; CD00172; "Configuring Digital Functions in a Digital Configurable Macro Architecture"; 07/18/01; 09/909,109; Snyder; 38 pages	<input type="checkbox"/>
6	From Application Number 10/033,027 (CD00173); "A Programmable Analog System Architecture (As Amended)"; 07/18/01; 09/909,047; Mar; 60 pages	<input type="checkbox"/>
7	From Application Number 10/033,027 (CD00174); "Programmable Methodology and Architecture for a Programmable Analog System (As Amended)"; 08/14/01; 09/930,021; Mar et al.; 87 pages	<input type="checkbox"/>
8	From Application Number 10/033,027; CD00175; "Method for Synchronizing and Resetting Clock Signals Supplied to Multiple Programmable Analog Blocks (As Amended)"; 10/01/01; 09/969,311; Sullam; 57 pages	<input type="checkbox"/>
9	From Application Number 10/033,027; CD00180; "Method and Apparatus for Programming a Flash Memory"; 06/05/01; 09/875,599; Snyder; 23 pages	<input type="checkbox"/>
10	Application Number 09/923,461 (CD00192); "Non-Interfering Multiply-Mac (Multiply Accumulate) Circuit;" Snyder; 25 pages 8/6/01	<input type="checkbox"/>
11	From Application Number 10/033,027; CD00187; "A Configurable Input/Output Interface for a Microcontroller"; 09/14/01; 09/953,423; Kutz et al.; 28 pages	<input type="checkbox"/>
12	From Application Number 10/033,027; CD00199; "Multiple Use of Microcontroller Pad"; 06/26/01; 09/893,050; Kutz et al.; 21 pages	<input type="checkbox"/>
13	From Application Number 10/033,027; CD00226; "Programming Architecture for a Programmable Analog System"; 08/14/01; 09/929,891; Mar et al.; 82 pages	<input type="checkbox"/>

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14	From Application Number 10/033,027: CD00227; "Architecture for Synchronizing and Resetting Clock Signals Supplied to Multiple Analog Programmable Analog Blocks"; 10/01/01; 09/969,313; Sullam; 50 pages	<input type="checkbox"/>
15	Application Number 10/011,214 (CD00191): "Method and Circuit for Synchronizing a Write Operation between an On-Chip Microprocessor and an On-Chip Programmable Analog Device Operating at Different Frequencies;" Sullam; 49 pages 10/25/01	<input type="checkbox"/>
16	USPTO Final Rejection for 09/994,601 (CD01170M) dated 03/08/2006; 14 pages	<input type="checkbox"/>
17	Application Number 09/935,454 (CD00194): "Method and Apparatus for Local and Global Power Management in a Programmable Analog Circuit;" Mar; 51 pages 8/22/01	<input type="checkbox"/>
18	USPTO Final Rejection for 09/994,601 (CD01170M) dated 03/24/2005; 13 pages	<input type="checkbox"/>
19	HINTZ ET AL.; "MICROCONTROLLERS", 1992, McGraw-Hill; 11 pages	<input type="checkbox"/>
20	GANAPATHY, GOPI, AND NARAYAN, RAM, AND JORDEN, GLEN, AND FERNANDEZ, DENZIL, AND WANG, MING, AND NISHIMURA, JIM; "Hardware Emulation for Functional Verification of K5", June 1996, 33rd Design Automation Conference Proceedings, 3-7 June 1996; 4 pages	<input type="checkbox"/>
21	The Provisional Application 60/243,708 (CD00167P) "Advanced Programmable Microcontroller Device"; 277 pages 10/26/00	<input type="checkbox"/>
22	"Webster's Third New International Dictionary", 1996, G. & C. Merriam Company; 3 pages	<input type="checkbox"/>
23	USPTO Advisory Action for 09/998,848 (CD01177) dated 09/07/05; 3 pages	<input type="checkbox"/>
24	Application Number 09/957,084 (CD00201): "A Crystal-Less Oscillator with Trimmable Analog Current Control for Increased Stability;" Mar; 28 pages 9/19/01	<input type="checkbox"/>

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25	Application Number 09/969,313 (CD00227): "Architecture for Synchronizing and Resetting Clock Signals Supplied to Multiple Analog Programmable System on a Chip Block;" Sullam; 50 pages 10/1/01	<input type="checkbox"/>
26	Application Number 09/972,319 (CD00179): "Method for Applying Instructions to Microprocessor in Test Mode;" Snyder; 31 pages 10/5/01	<input type="checkbox"/>
27	Application Number 09/972,003 (CD00176): "Test Architecture for Microcontroller Providing for a Serial Communication Interface;" Snyder; 32 pages 10/5/01	<input type="checkbox"/>
28	Application Number 09/972,133 (CD00178): "Method for Entering Circuit Test Mode;" Snyder; 30 pages 10/5/01	<input type="checkbox"/>
29	Application Number 09/973,535 (CD00193): "Architecture for Decimation Algorithm;" Snyder; 26 pages 10/9/01	<input type="checkbox"/>
30	Application Number 09/977,111 (CD00202): "A Frequency Doubler Circuit with Trimmable Current Control; Shutt; 35 pages 10/11/01	<input type="checkbox"/>
31	Application Number 10/272,231 (CD00170C): "Digital Configurable Macro Architecture;" Snyder; 36 pages 10/15/02	<input type="checkbox"/>
32	Application Number 11/125,554 (CD00230C): "A Method for a Efficient Supply to a Microcontroller;" Kutz; 1 page 5/9/05	<input type="checkbox"/>
33	Application Number 09/855,868 (CD00181): "Protecting Access to Microcontroller Memory Blocks;" Snyder; 28 pages 5/14/01	<input type="checkbox"/>
34	Application Number 09/887,923 (CD00229): "Novel Method and System for Interacting between a Processor and a Power on Reset to Dynamically Control Power States in a Microcontroller;" Kutz; 44 pages 6/22/01	<input type="checkbox"/>
35	Application Number 10/000,383 (CD00189): "System and Method of Providing a Programmable Clock Architecture for an Advanced Microcontroller;" Sullam; 34 pages 10/24/01	<input type="checkbox"/>

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	36	Application Number 10/001,477 (CD01203M): "Breakpoint Control in an In-Circuit Emulation System;" Roe; 43 pages 11/1/04	<input type="checkbox"/>
	37	Application Number 10/004,197 (CD01220M): "In-Circuit Emulator with Gatekeeper Based Halt Control;" Nemecek; 47 pages 11/14/01	<input type="checkbox"/>
	38	Application Number 10/004,039 (CD01222M): "In-Circuit Emulator with Gatekeeper for Watchdog Timer;" Nemecek; 46 pages 11/14/01	<input type="checkbox"/>
	39	Application Number 10/002,217 (CD01207M): "Conditional Branching in an In-Circuit Emulation System;" Nemecek; 43 pages 11/1/01	<input type="checkbox"/>
	40	Application Number 10/001,568 (CD01214M): "Combined In-Circuit Emulator and Programmer;" Nemecek; 47 pages 11/1/01	<input type="checkbox"/>
	41	Application Number 10/001,478 (CD01213M): "In-Circuit Emulator and POD Synchronized Boot;" Nemecek; 44 pages 11/1/01	<input type="checkbox"/>
	42	Application Number 09/887,955 (CD00197): "Novel Power on Reset Circuit for Microcontroller;" Kutz; 42 pages 6/22/01	<input type="checkbox"/>
	43	Application Number 09/826,397 (CD00190): "Method and Circuit for Allowing a Microprocessor to Change its Operating Frequency on-the-fly;" Sullam; 24 pages 4/2/01	<input type="checkbox"/>
	44	Application Number 09/893,048 (CD00231): "A Microcontroller having an On-Chip High Gain Amplifier;" Kutz; 22 pages 6/26/01	<input type="checkbox"/>
	45	Application Number 09/912,768 (CD00200): "A Microcontroller having a Dual Mode Relax Oscillator that is Trimmable;" Shutt; 33 pages 7/24/01	<input type="checkbox"/>
	46	Application Number 09/922,419 (CD00198): "A Power Supply Pump Circuit for a Microcontroller;" Kutz; 38 pages 8/3/01	<input type="checkbox"/>

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47	Application Number 09/922,579 (CD00230): "A Method for a Efficient Supply to a Microcontroller;" Kutz; 37 pages 8/3/01	<input type="checkbox"/>
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